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Getting Started in (z/OS) Capacity Planning (Topics in Capacity Planning) Part I


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Bibliography

Ray has spent most of his career at IBM in the performance analysis and capacity planning end of the business in Poughkeepsie, London, and now at the Washington Systems Center. He is the major contributor to IBM's internal PA & CP tool zCP3000. This tool is used extensively by the IBM services and technical support staff world wide to analyze existing zSeries configurations (Processor, storage, and I/O) and make projections for capacity expectations.

Ray has given classes and lectures worldwide. He was a visiting scholar at the University of Maryland where he taught part time at the Honors College.

He won the prestigious Computer Measurement Group's A.A. Michelson award in 2000. His recent virtual sessions "Getting Started in Performance Analysis & Capacity Planning" workshop held for attendees in China and India was well accepted.

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Abstract

This tutorial is a **two** part introductory level session designed to introduce the student to the concepts required for Performance Analysis and Capacity Planning.

Emphasis is placed on large processor systems and examples will be largely drawn from z/OS but the concepts apply to all operating systems and hardware. The tutorial is organized to review the architecture where appropriate (albeit briefly). Topics:

- Conceptual and Perceptual structures for performance analysis and capacity planning,
- Using the Forced Flow law in PA & CP
- Performance Analysis queries for capacity planning,
- Processor performance data (ITRRs & MIPS),
- Resource Metrics for use in the Balance System model,
- Sample selection,
- Data preparation in z/OS,
- Using the utilization growth process in capacity planning,

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Capacity Planning

Capacity Planning ensures that **Adequate** resources are available for the Workload to complete in an **Appropriate time**.

- ❑ Performance Analysis is Short Term (3-7 Days)
- ❑ Capacity Planning is Long Term (6-24 Months)
- ❑ What's the Workload using now?
- ❑ What's Appropriate? Adequate?
- ❑ Service Level Objective or Service Level Agreement
- ❑ Things are ordered by Priority or Importance
- ❑ Discretionary Workloads?

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CP Questions

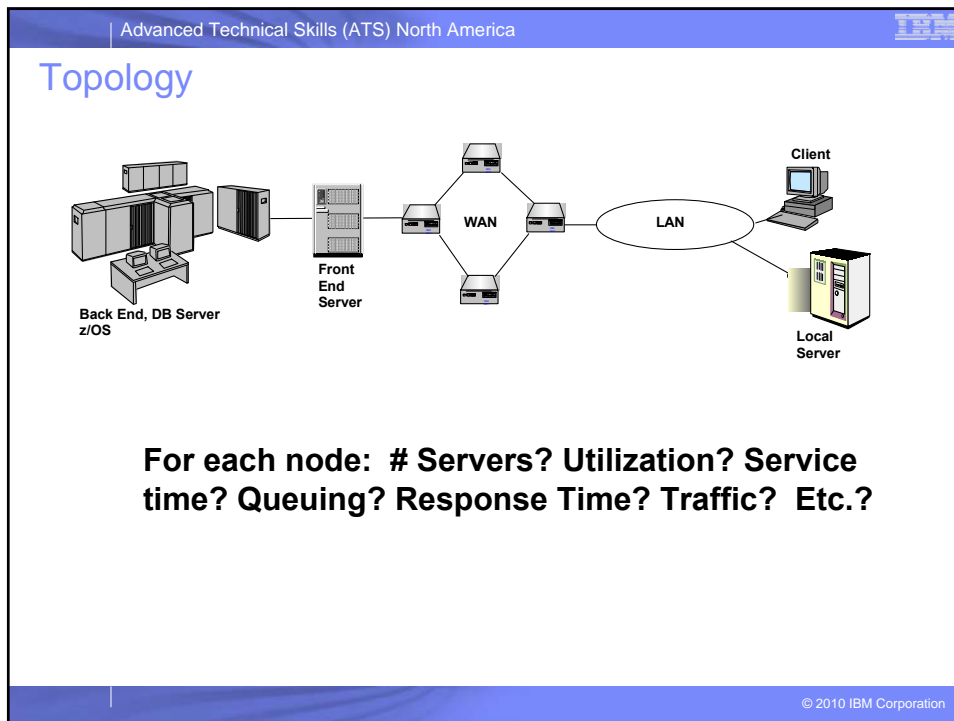
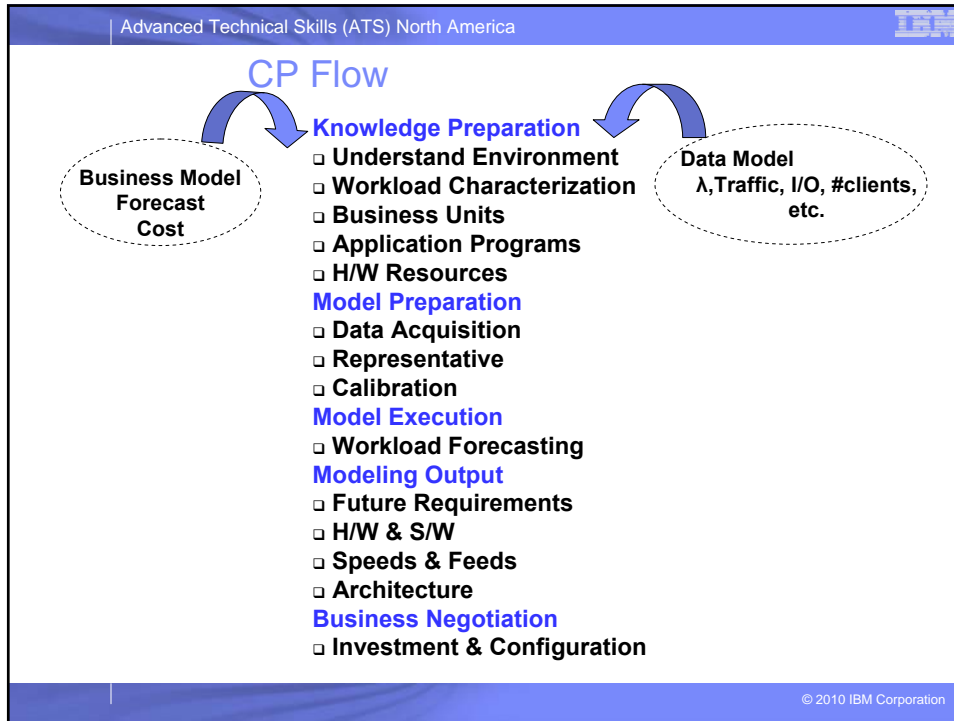
EASY

- ❑ Do I have enough resource (CPU, I/O, Storage,...) to do the job today?
- ❑ If not, who's suffering?
- ❑ If I get more, who will be helped? How much?
- ❑ If I need more, when will it be?
- ❑ How much more?
- ❑ Can I use specialized Processing Units?
- ❑ What variables should I track?
- ❑ Do I have any latent demand?

Harder

- ❑ Do I want faster or more CPs?
- ❑ How do I establish my growth?
- ❑ How do I size a new application?
- ❑ What tools should I use?
- ❑ Which interval do I model?
- ❑ If I reduce the #CPs & keep the MIPS the same will there be a problem?

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Lots of Data

The image displays a complex dashboard with multiple data visualization components:

- Table:** A large table at the top showing system metrics with columns for 'Num_Samples', 'Date', 'Time', 'Duration', 'Sys_ID', 'CPU_Model', 'CPS', 'CPFS', 'PLS', 'AAAPS', 'SUPRV', 'LPAR_Name', and 'SYS'. It lists various system configurations and their performance metrics.
- IO Intensity in MB:** A bar chart showing IO intensity for different systems, with 'SYS1' having the highest intensity.
- Workload Phys Util for RAY:** A stacked bar chart showing physical utilization for 'RAY' across different periods.
- Health Check for SYSD AGFT:** A chart showing the health status of 'SYSD AGFT' across various system components like System, LPAR, SYSD, Memory, Work, PAF, Data, and Tape.
- BCU Response Intensity for SYS1:** A bar chart showing BCU response intensity for 'SYS1'.
- System Information:** A central window displaying system information for 'SYS1' and 'RAY', including 'Average Number of Active Users' and 'System Health Data'.

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Frameworks

Conceptual Framework (Model, Paradigm)

- What's it supposed to mean?
- How do things connect?

Perceptual Framework

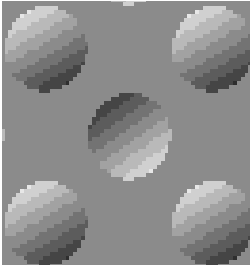
- What's it supposed to look like?
- How do things connect?

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The Danger of a Framework

Turn 180°

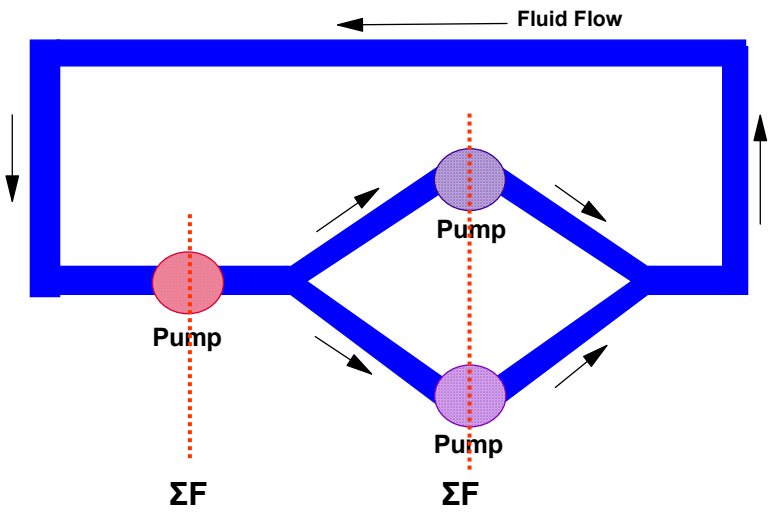


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Detailed description: This slide features a blue header with the text 'Advanced Technical Skills (ATS) North America' and a small IBM logo. The main title is 'The Danger of a Framework'. Below the title, the text 'Turn 180°' is centered. Underneath, there is a square image containing a 3x3 grid of spheres. The central sphere is rotated 180 degrees, while the surrounding eight spheres are not. This visual metaphor likely represents a framework where one element is out of phase or inverted relative to the rest.

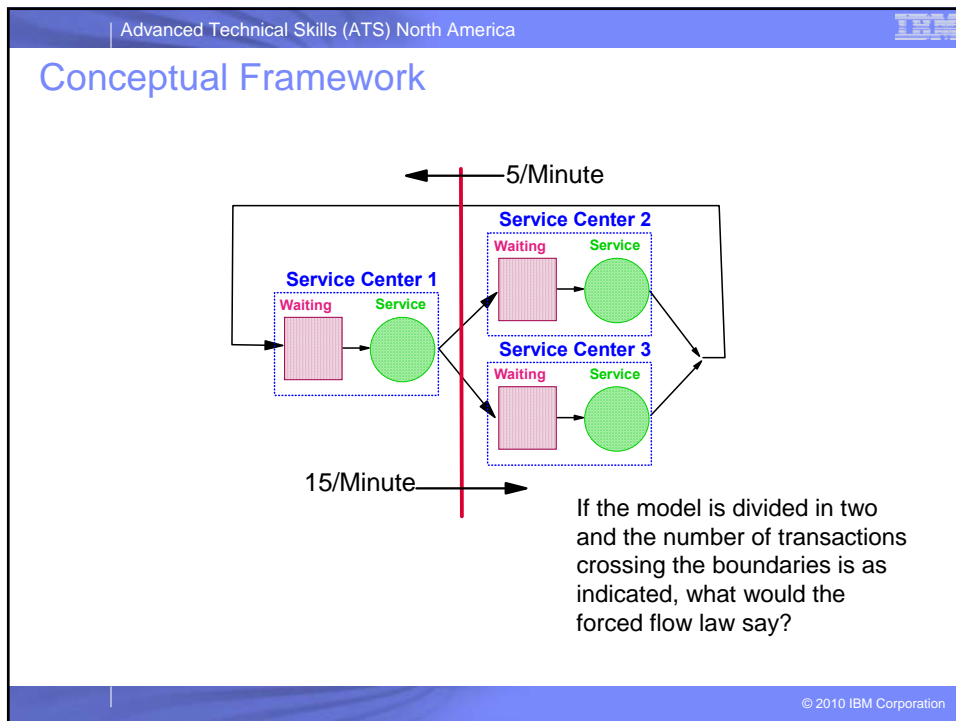
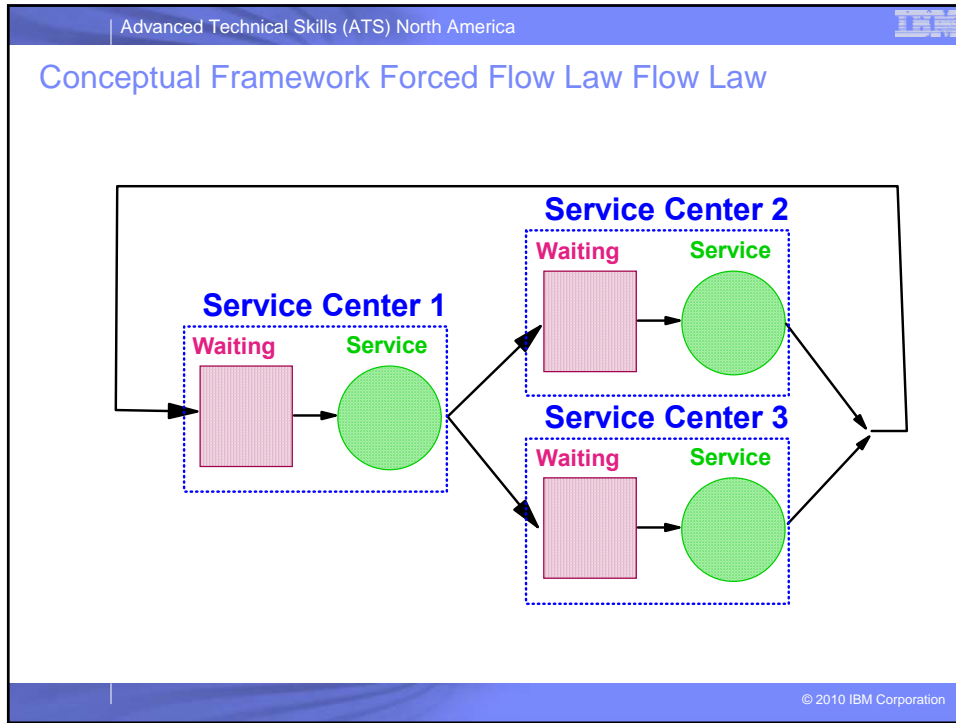
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A Plumbing Problem



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Detailed description: This slide features a blue header with the text 'Advanced Technical Skills (ATS) North America' and a small IBM logo. The main title is 'A Plumbing Problem'. The diagram shows a blue piping system. On the left, a red circle labeled 'Pump' is connected to a vertical pipe that goes down and then right. This pipe then splits into two parallel paths, each containing a purple circle labeled 'Pump'. These two paths rejoin and then go up and right to a vertical pipe that goes up and then left. A horizontal pipe at the top connects the left and right vertical pipes, with an arrow labeled 'Fluid Flow' pointing to the left. Below each of the three pumps is a vertical dashed red line labeled with the Greek letter sigma followed by F (ΣF).



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Conceptual Framework

If the total time in the service center is as indicated, where would the users/transactions be?

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B.S. Conceptual Framework

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Conceptual Framework Implications

- Users distribute themselves among nodes in proportion to the time spent at each node.
- The capacity of the **System** is determined by the slowest node (server).
- **The resource usage (transaction rates) at various nodes are in proportion.**

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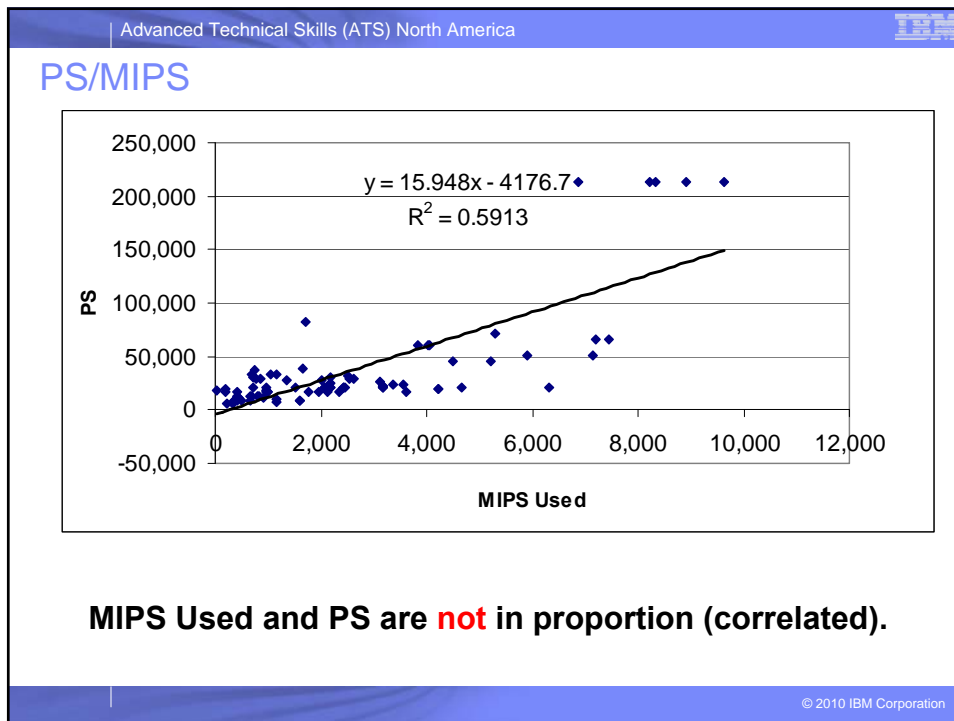
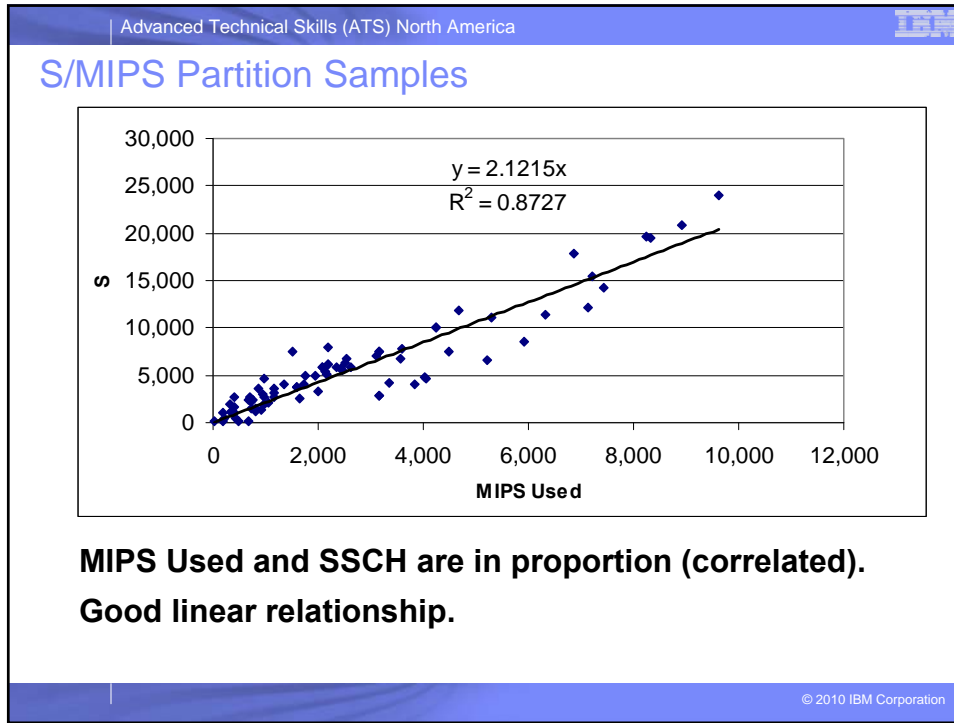
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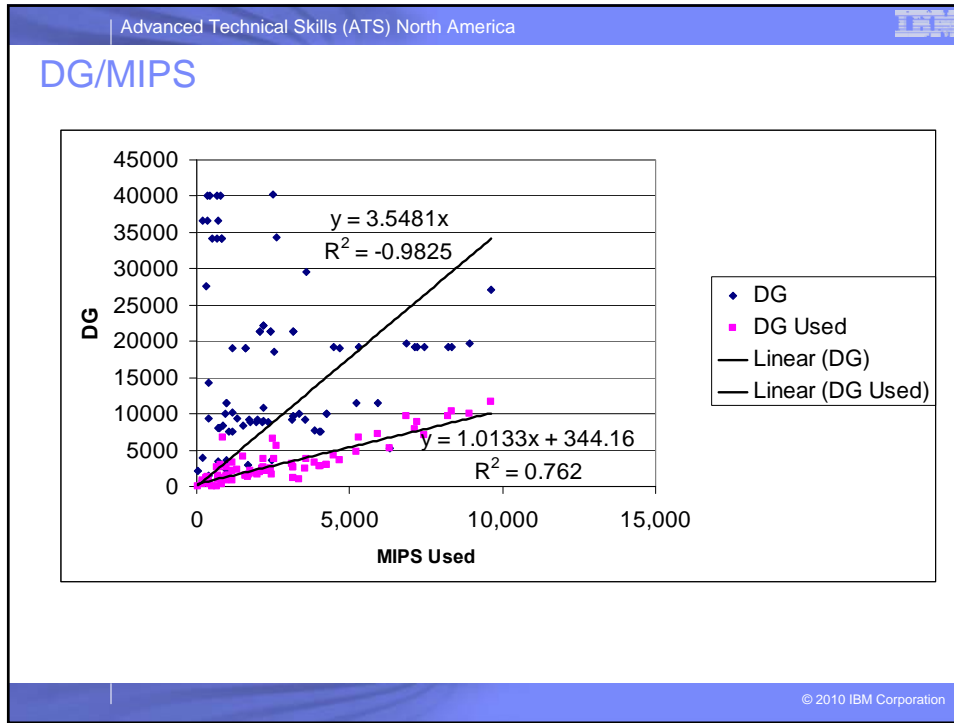
2010 z/OS B.S. Metrics

MIPS used
 S = SSCH rate
 DG = DASD gigabytes. The computation is nominal in that it is 2.83/act
 PS = Central Storage configured

	10%	50%	90%
S/MIPS	1.201	2.349	3.707
DG/MIPS	2.236	6.593	52.539
PS/MIPS	6.766	14.055	37.306

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Desperate Capacity Planning

**What part of a 500 MIPS machine would DB2 use at 100 I/Os per second?
How many MIPS for DB2?**

S/MIPS ∈ [1.201, 2.349, 3.707]
 100/MIPS = 1.201 → MIPS = 83.26
 100/MIPS = 2.349 → MIPS = 42.57

Answer:
 Between 8.5% and 16.6% (????) of a 500 MIPS Machine

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More Metrics Defined

MIPS used
S = SSCH rate

DG = DASD gigabytes. The computation is nominal in that it is 2.83/act
DG = Nacts * 2.83

CS = Central Storage configured
D as in CS = 4000 + 0.04(MIPS)^D
 where CS (or PS) is configured Central Storage (or RAM)

NNacts = acts with rate >=2
AD = Access Density = S/DG

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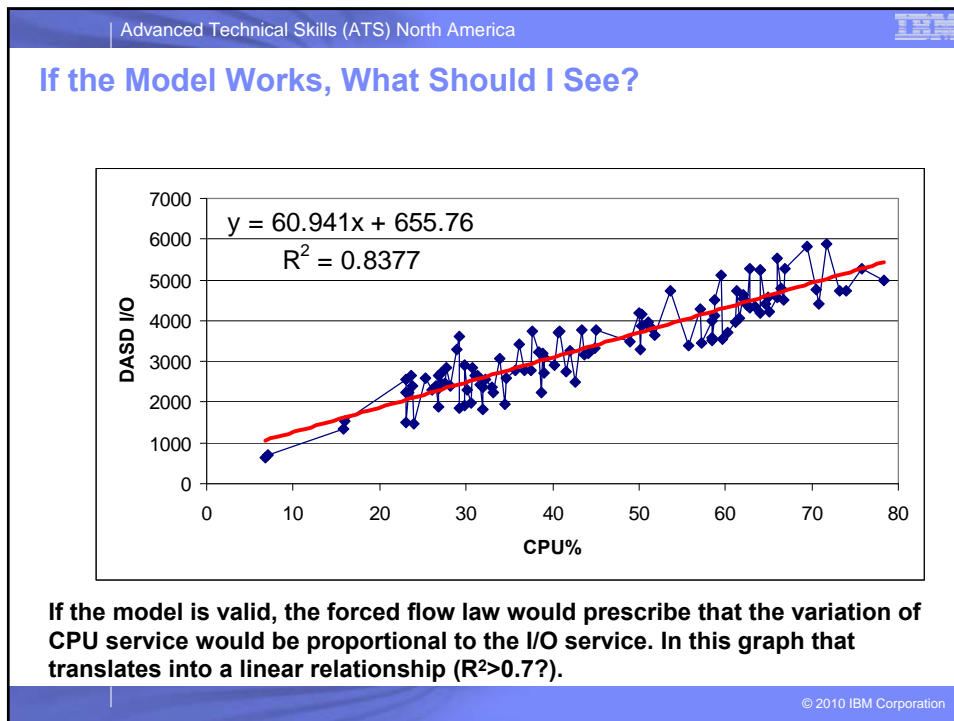
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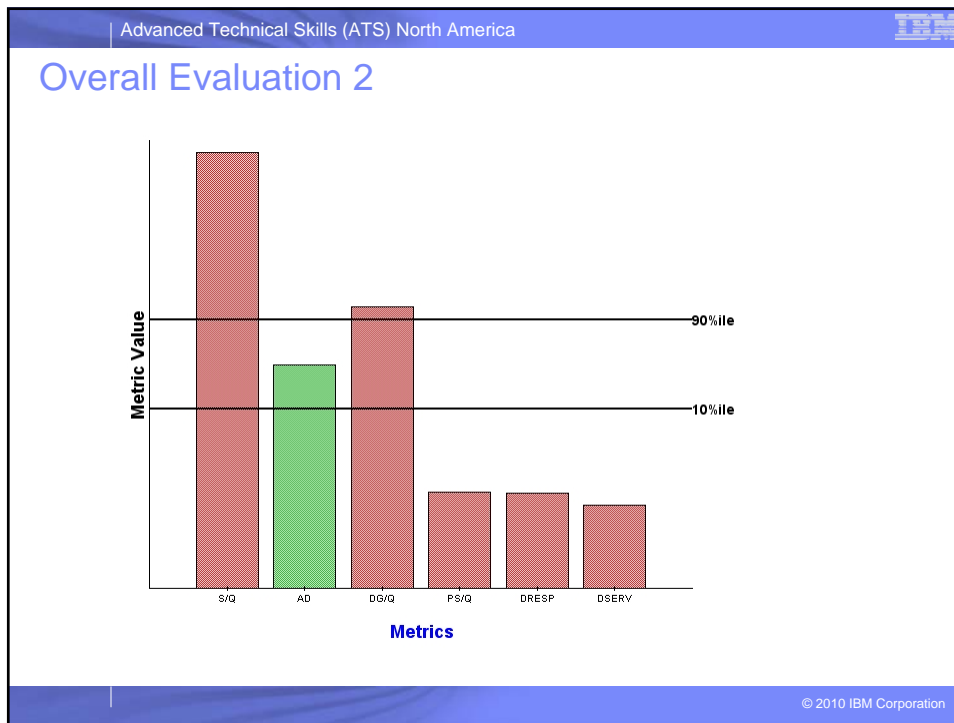
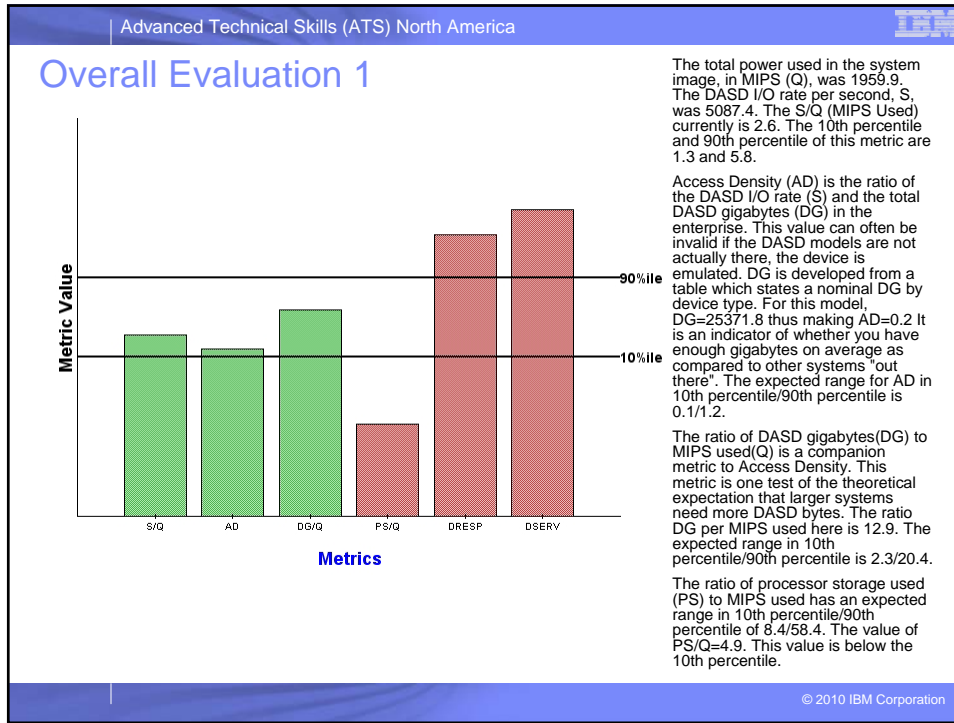
Full Metrics

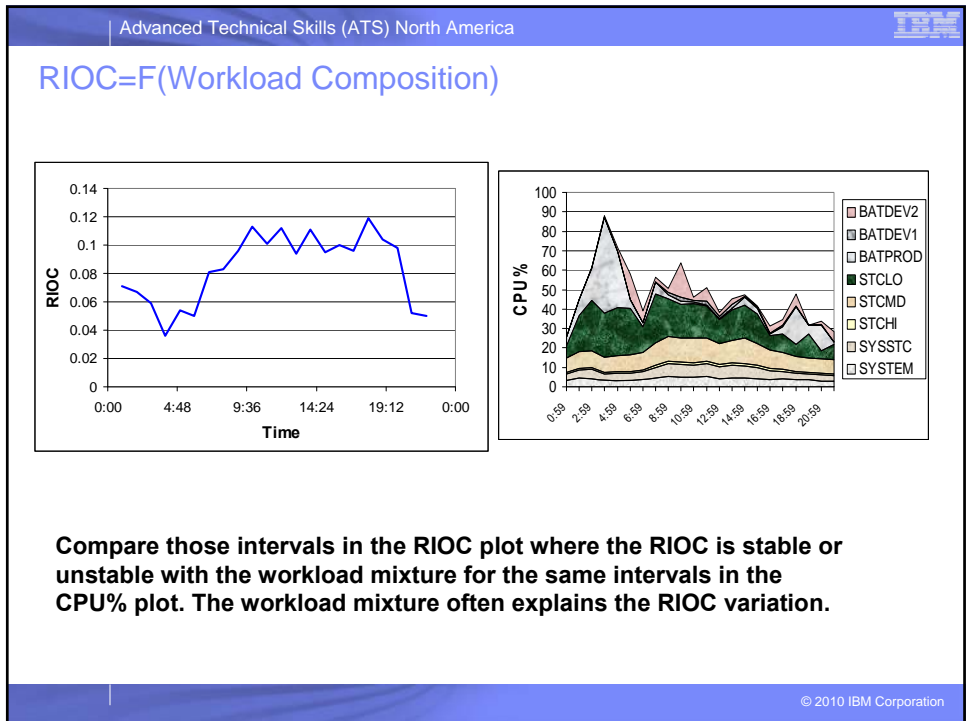
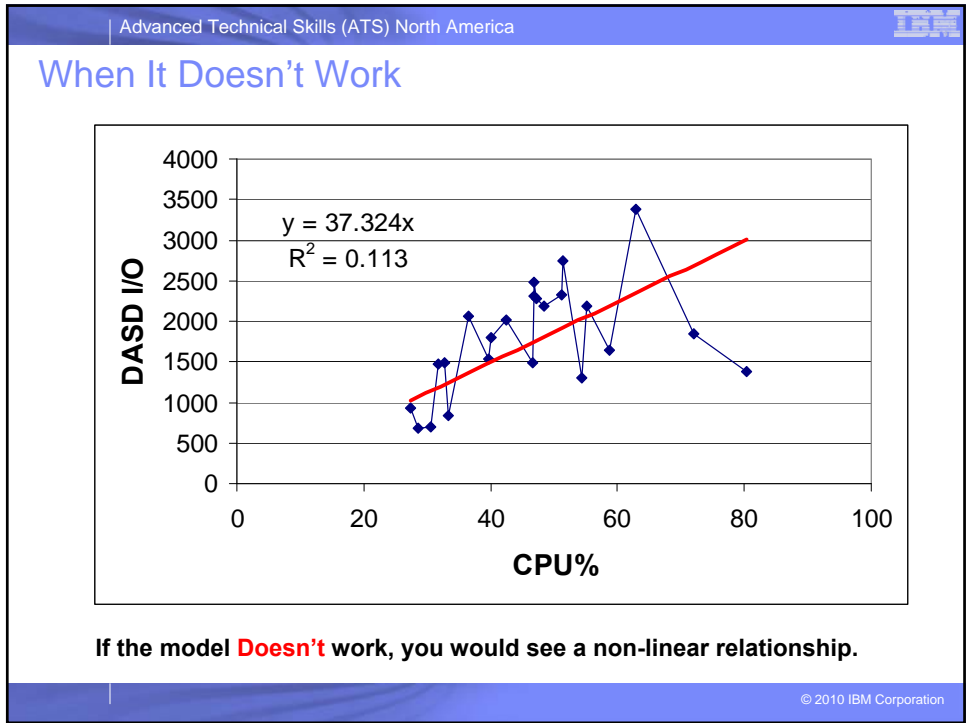
2010 83 Partitions

	10%	50%	90%
MIPS	403	2004	6247
S	1123	4221	11779
S/MIPS	1.201	2.349	3.707
DG/MIPS	2.236	6.593	52.539
PS/MIPS	6.766	14.055	37.306
D	1.534	1.630	1.894
DASD Resp	0.952	1.865	3.626
DASD Serv	0.681	1.564	2.980
Resp/Serv	1.227	1.827	1.232
Nacts	1305	4084	12105
NNTacts	73	264	882
DASD GB	3693	11558	34256
Used DG	585	2114	7065
AD	0.059	0.360	0.909

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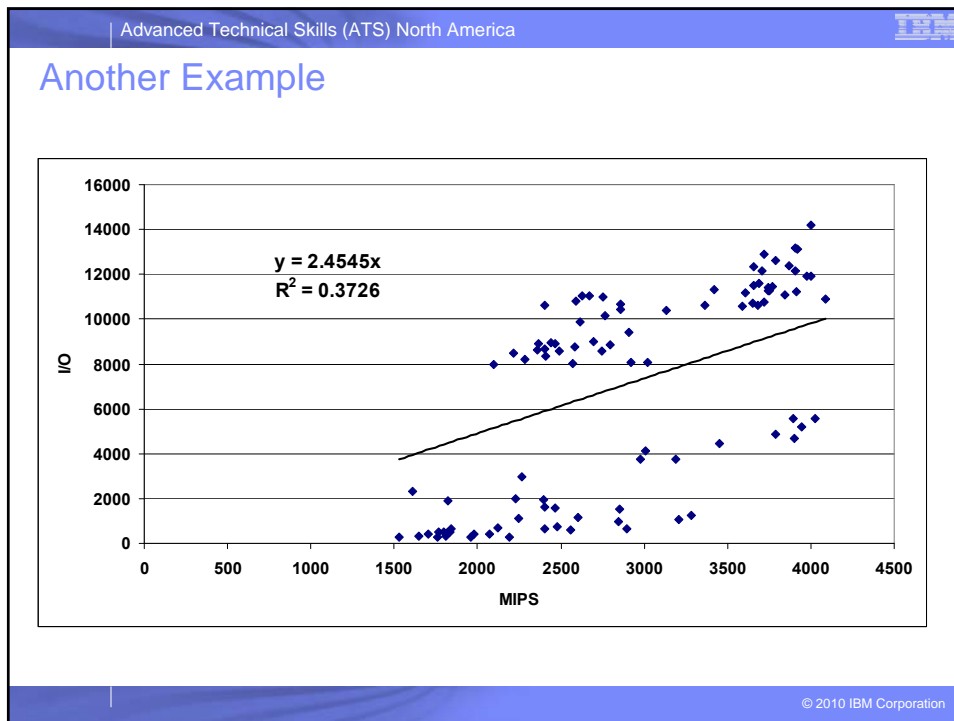


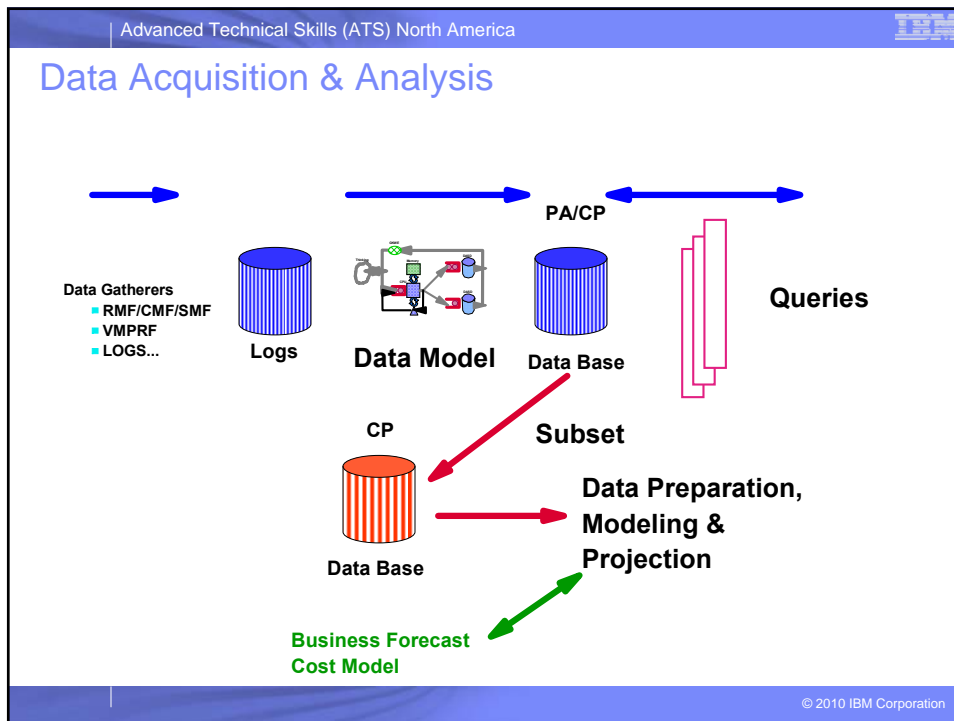
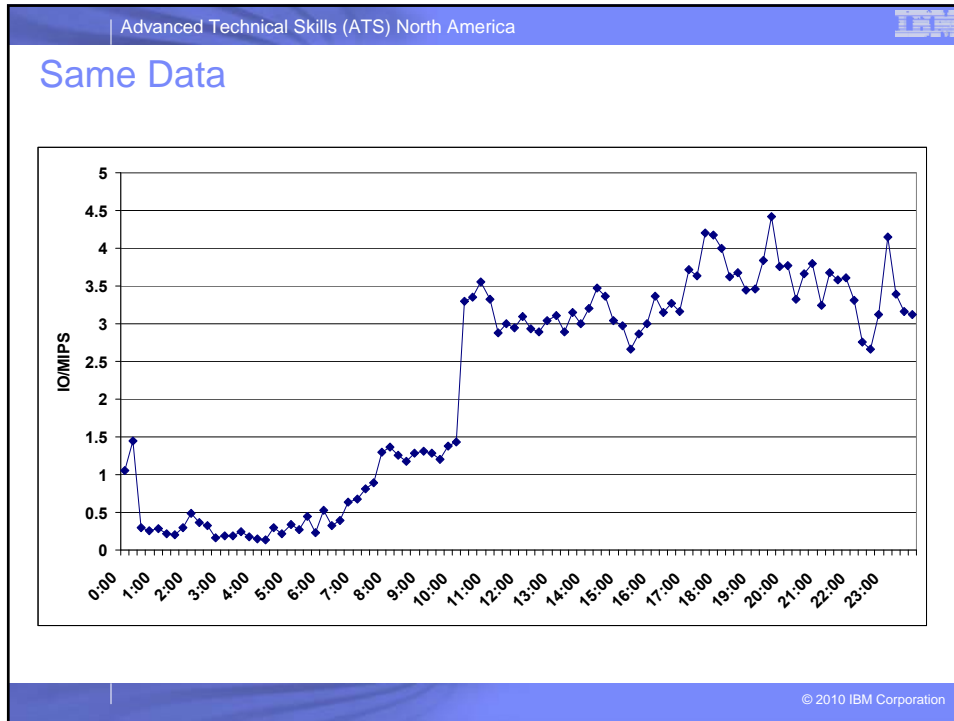
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Use of Conceptual Framework like BS Model

- ❑ **Establishes Relationships**
 - ❑ Balanced System, resource ratios
- ❑ **Builds Expectations**
 - ❑ Linear graph
- ❑ **High lights Exceptions**
 - ❑ Non Linearity
 - ❑ Outliers
- ❑ **Generates Questions**
 - ❑ Especially if not as expected
- ❑ **But... it may cause you to see Framework (model) interactions that just aren't there!**

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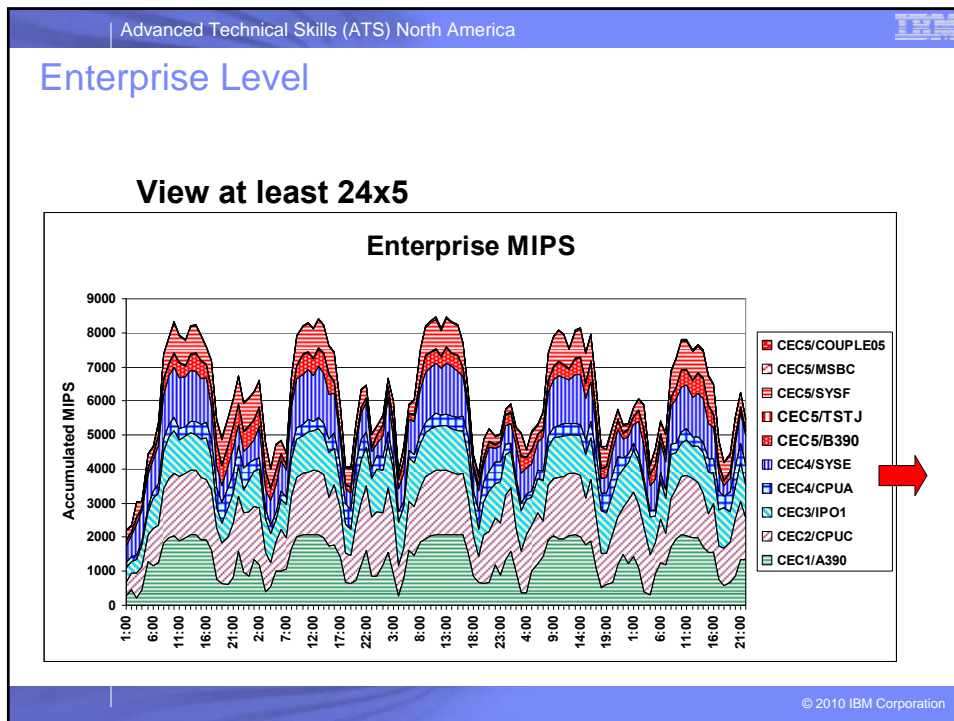


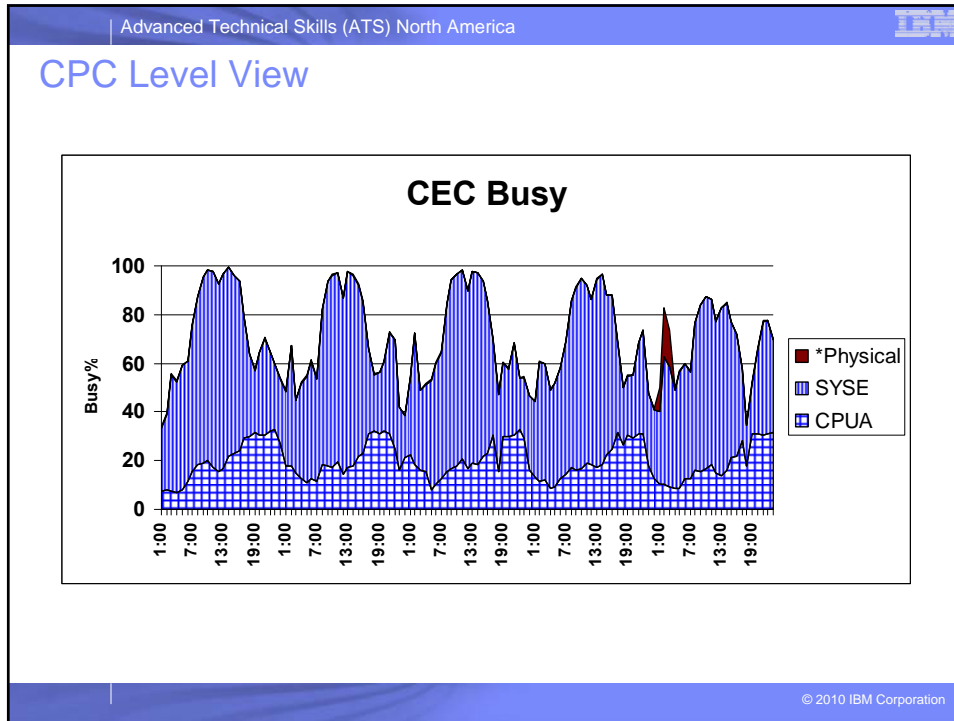
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Performance Analysis Queries of Interest to CP

- Enterprise Level MIPS Usage
- CPC Level Busy
- System Image (Partition) Level
 - Workload Busy CPU and I/O Intensity
 - Contention Indicators
 - Average in and Ready (AINR)
 - OCPU1 = %AINR >= 1+#CPs
 - OCPU2 = %AINR >= 2+#CPs
 - Average Out and Ready (AOUTR)
 - Delays by Workload
 - Performance Index >1

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CPU%

# Engines	Maximum ST in Seconds	Seconds if CPU%=42%
1	1	0.42
2	2	0.84
3	3	1.26
4	4	1.68
5	5	2.10

0.84 Secs or 840 Ms.

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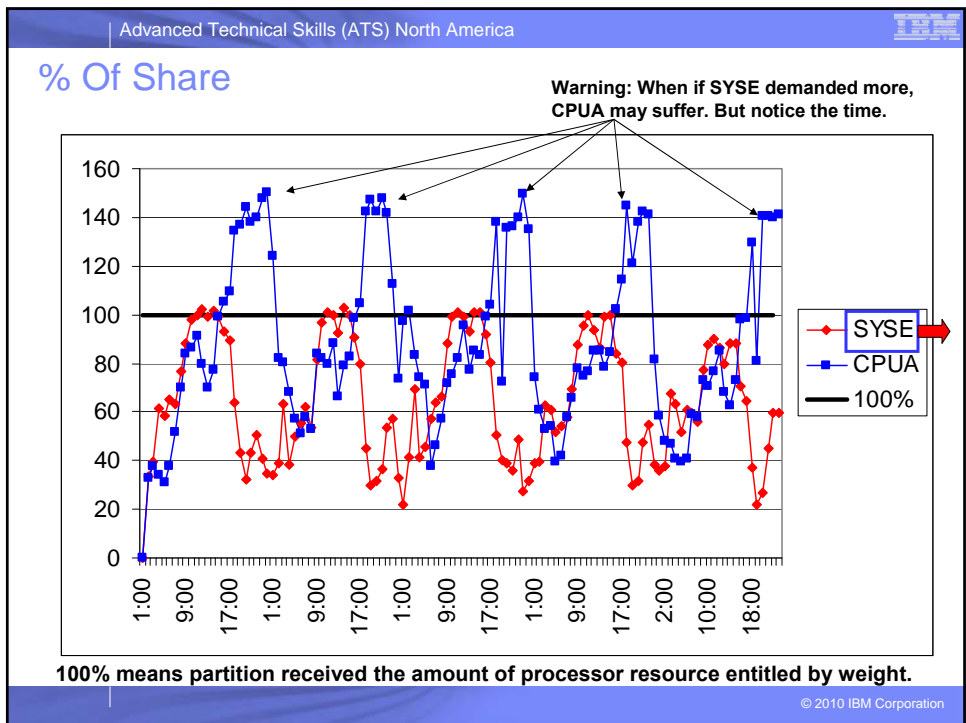
Weights

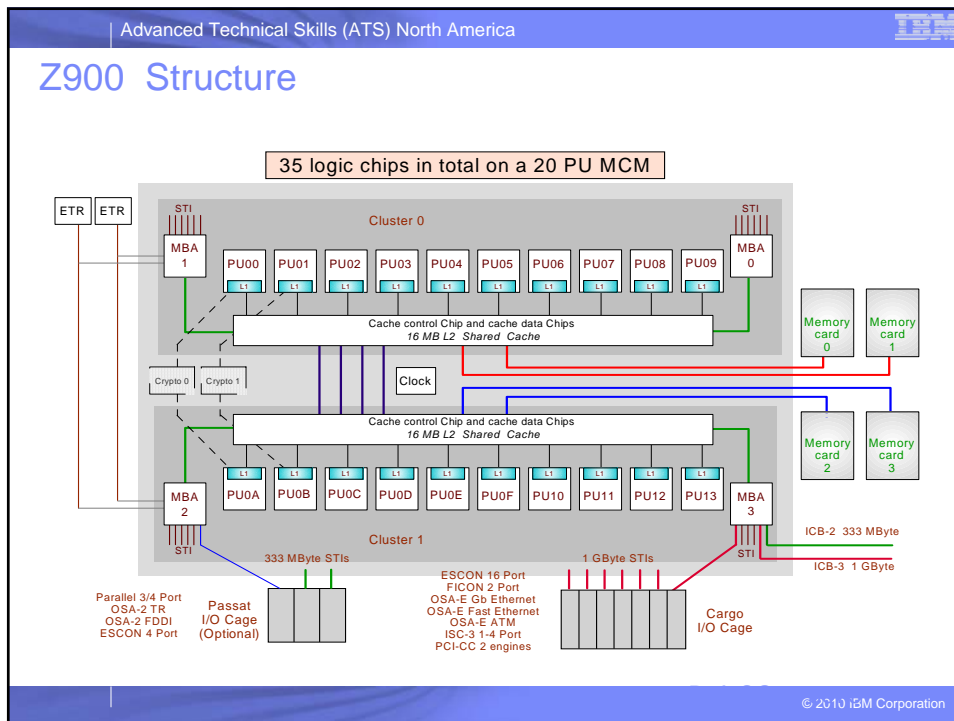
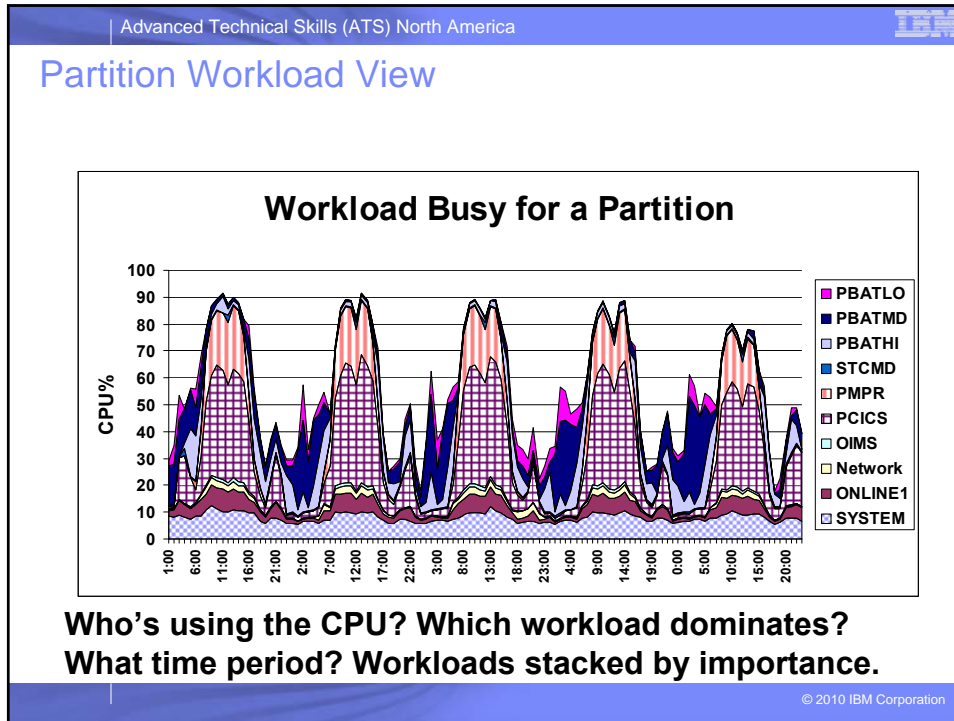
Weight as Percent: When utilization of CPC gets to 100%, this partition should get this percent of the Shared CPU pool.

10 CP CPC

Part	#LCPS	SHARED	Weight	Weight %	Logical CPU%	Physical CPU%	Used (#CPs)	Entitlement (#CPs)
LPAR1	2	Y	300	25%	50%	10%	1	2.5
LPAR2	3	Y	400	33%	67%	20%	2	3.3
LPAR3	6	Y	500	42%	100%	60%	6	4.2

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Connections

The diagram illustrates the connection between a Buffer and a Micro Processor (Adaptor). On the left, a box labeled 'Buffer' contains a blue and red bar and a circular arrow icon. On the right, a box labeled 'Micro Processor (Adaptor)' contains a similar bar and icon. A central 'Link' connects the two boxes, with 'SubChannels' indicated by arrows pointing to the link. Arrows show data moving from the Buffer's buffer into the Link, then into the Micro Processor's buffer, and finally into its storage buffer.

Every line connecting two boxes in a diagram implies micro processors on each end to do the talking? (What happens if they speak different languages?) Data is moved from a buffer to micro processor buffer onto link into m-processor buffer into storage buffer.

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Z10 Memory – a simple view

The diagram shows the memory hierarchy for the Z10. At the top, 'Memory' is connected to 'L2 Cache'. Below the L2 Cache, there are 'L1.5' and 'L1' caches. The 'L1.5' caches are connected to the 'L2 Cache', and the 'L1' caches are connected to the 'L1.5' caches. The 'L1' caches are connected to 'CPU' units. A red box labeled 'The Nest' encloses the Memory, L2 Cache, and L1.5 caches. A 'Book' icon is positioned between the two memory paths. The entire structure is supported by a 'PR/SM' base.

Reference: John Burg's presentation at SHARE 8/4/2010
<http://www.ibm.com/support/techdocs/atmastr.nsf/Webindex/TC000066>

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